

2 Day Workshop (21st-22nd Feb 2020)

VLSI & Embedded Systems

Organized
by

Department of Electronics & Communication Engineering
Central Institute of Technology-Kokrajhar, Kokrajhar,
Assam, India, PIN-783370
www.cit.ac.in

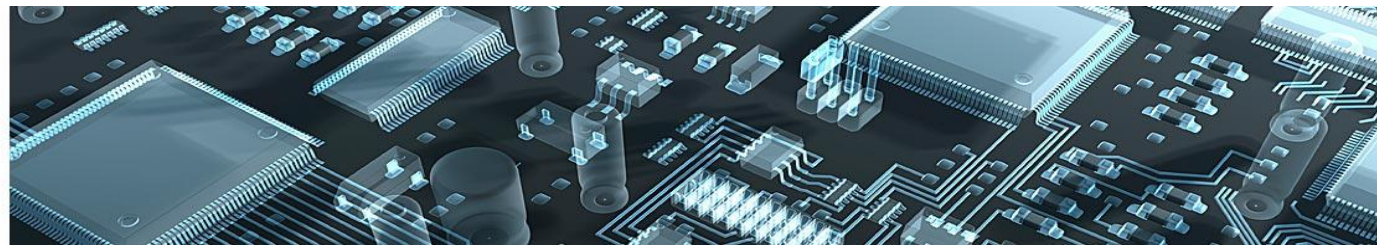


Course Information

VLSI design & Embedded Systems has both been the key areas with many exciting job opportunities. This workshop will enable you to learn from peers in the areas of FPGAs, Memory cells etc.. It also covers up the basics of Embedded Systems, sensors used in these systems and use of Embedded C for Microcontrollers interfacing with I/Os, which would be supported with hands on sessions. It will provide combination of lectures and hands-on practice on VLSI and Embedded based System Design.

Topics to be covered

- VLSI Design Flow and introduction to CAD Tools.
- Creating Schematics and Layouts for Digital circuits.
- Embedded System Design Flow.
- Sensors and their applications in Embedded Systems
- Introduction to Microcontrollers.
- Introduction to Embedded C for Microcontroller.
- Hands On session for Programming Microcontrollers Using Embedded C using ARM Keil/ supporting softwares.



Registration form

1. Name : _____
(Capital Letters with Surname)
2. Designation: _____
3. Organization/Institution: _____
4. Corresponding Address : _____

5. Mobile No. : _____
6. Email ID : _____
7. Gender (M/F) : _____
8. Payment Details: Payment enclosed
(DD No. _____ Date _____, Drawn from
_____ Amount _____)

Date:

Signature

Registration & Venue

Online and Offline registration facilities are available, both of which can be performed by paying Rs. 100/- in favor of CIT-Kokrajhar on or before **18th February 2020**. Registration Fee for candidates **other than** students of **CIT-Kokrajhar is Rs. 200/-**.

A/C Name: CIT Kokrajhar
Acc. No: 30495666063
IFSC Code: SBIN007379

Bank Name: State Bank of India- North Kokrajhar
*Registration Fee includes Registration kit and Tea/Snacks without lunch. (No accommodation support will be provided)

Venue: Department of ECE, CIT-Kokrajhar, Assam

Resource Persons

Academicians/ Professors :

- **Dr. Manabendra Bhuyan, Professor, Tezpur University**

Industry Persons:

- **Mr. Debasis Sahu, Layout Engineer, Broadcom-Bangalore.**

- **Mr. Prateek Pandey, FPGA Services, Swedium Global-Bangalore.**

Eligible Criteria

Undergraduates completing Basic VLSI Design, Microprocessors/ C Programming will benefit largely from this workshop. Please bring Laptops.

About The Institute

The Institute was established on the 19th of December 2006. The genesis of this Institute was the memorandum of Settlement on Bodoland Territorial Council (BTC) signed between the Assam Government, the Union Government and the Bodo Liberation Tigers, on February 10, 2003, in New Delhi. The Institute is an autonomous body registered under the Societies Registration Act., 1860 and functions under a Board of Governors (BOG). It's a Deemed to be University under MHRD, Govt. of India.

Coordinators

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